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1. [PDF] **Register Allocation** Previously • Graph Coloring Allocators ...

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Linear order of **code** ignores dynamic control flow. • A temporary may reside in different locations **...** After **register allocation**: 1. **dead-code** elimination **...**
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2. libjit-**linear**-scan-**register**-allocator - Project Hosting on Google **Code**

Dead-code elimination (level 4 of optimization). Static live variable analysis (level 3, 4 of optimization). **Linear** scan **register allocation** algorithm **...**
code.google.com/p/libjit-**linear**-scan-**register**-allocator/ - Cached - Similar

3. **Linear** IR - Mono

This pass performs **dead code** elimination on single basic blocks. **...** registers available for global **register allocation**. It uses a **linear** scan algorithm. **...**
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4. [PDF] Quality and Speed in **Linear**-scan **Register Allocation**

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to **register allocation**, called **linear** scan, and we compare it **.....** each case, **register allocation** is preceded by **dead code** elim- **...**
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5. A Modified **Linear** Scan **Register Allocation** Algorithm

opposed to **linear** scan **register allocation** live ranges along **...** The algorithm produces **code** that is better than **linear** scan **register allocation** in terms of speed of **....** searched for intervals that are **dead** i.e. whose end points **...**

doi.ieeecomputersociety.org/10.1109/ITNG.2009.90 - Similar
by S Subha

6. **Linear** Scan **Register Allocation** in a High-Performance Erlang Compiler

We have experimented with **register allocation**, focusing on **linear** scan, and reported
on its performance in the context of a just-in-time native **code ...**
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7. [PS] **Linear**-Scan **Register Allocation** in Region-based Compilation

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Linear-Scan **Register Allocation**. Two important goals guide the design of our
register allocation algorithm: speed. of **allocation** and quality of **code ...**
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8. [PS] **Linear** Scan **Register Allocation** in the hipe Compiler

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the temporary is **dead** at all instructions outside the live interval; **.....** the **code** so much
that the SCC-based **linear** scan **register allocation** becomes **...**
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9. [PDF] Quality and Speed in **Linear**-Scan **Register Allocation**

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run copy propagation after **register allocation**. Finally, a **dead**-**code** elimination pass
is added to remove unnecessary instructions and basic blocks from the **...**
reference.kfupm.edu.sa/.../quality_and_speed_in_linear_scan_registe_107391.pdf
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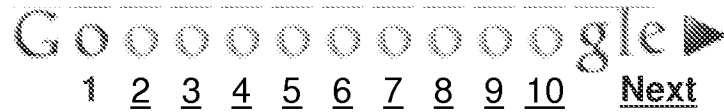10. Experimental evaluation and improvements to **linear** scan **register ...**

LINEAR SCAN **REGISTER ALLOCATION**. 1013 with a compiler for, e.g., C or
FORTRAN **code** are the relatively high number of function calls, the **...**
doi.wiley.com/10.1002/spe.533 - Similar

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